APPENDIX A

1. **Patent Application/Serial No.:** 09/561,363

    Applicant(s): Grzegorz Czajkowski

    Title: Method and Apparatus for Optimized Multiprocessing in a Safe Language

    Filed: April 28, 2000

    Reel/Frame: 010770/0409

2. **Patent Application/Serial No.:** 09/648,088

    Applicant(s): William K. Lam

    Title: Method and Apparatus for Memory and Performance Optimization of Logic Simulation

    Filed: August 25, 2000

    Reel/Frame: 011039/0504

3. **Patent Application/Serial No.:** 09/858,992

    Applicant(s): Jeffrey J. Dunn

    Title: Method and Apparatus for Automatic Accessibility Assessment

    Filed: May 15, 2001

    Reel/Frame: 011819/0041

4. **Patent Application/Serial No.:** 09/779,322

    Applicant(s): Bernd J.W. Mathiske, et al.

    Title: Method and Apparatus for Bag-To-Set, Buffering Remembered Set

    Filed: February 8, 2001

    Reel/Frame: 011540/0801

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5.   Patent Application/Serial No.: 09/738,936

        Applicant(s): Keith R. Hargrove

            Title: Method and Apparatus for Desirable Network
                    Components

            Filed: December 15, 2000

        Reel/Frame: 011359/0966

6.   Patent Application/Serial No.: 09/624,445

        Applicant(s): Stephen Uhler, et al.

            Title: Architecture for Building Web Applications

            Filed: July 24, 2000

        Reel/Frame: 011251/0865

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